

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	23	multiplexer\$1 with (pixel\$1 adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:40
L3	364289	(lcd or liquid adj crystal adj display)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:35
L4	5	2 and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:35
L12	8	multiplex\$3 and (tft adj thin adj film adj transistor\$1) and pixel\$1 adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:53
L13	344232	multiplex\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:53
L14	4836	(tft adj thin adj film adj transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:53
L15	2751	pixel\$1 adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:54
L16	0	13 adj 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:54

L17	12	13 with 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:56
L18	24	13 same 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:27
L19	54	(multiplexer or switch\$3 or distributer) adj (tft adj thin adj film adj transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:45
L20	57775	(sram or static adj memor\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:31
L21	48	14 with 20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:31
L26	0	20 and 19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:44
L27	39	3 and 19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:44
L31	2	(multiplexer or distributer) and (tft adj thin adj film adj transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:46

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	23	multiplexer\$1 with (pixel\$1 adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:40
L3	364289	(lcd or liquid adj crystal adj display)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:35
L4	5	2 and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:35
L12	8	multiplex\$3 and (tft adj thin adj film adj transistor\$1) and pixel\$1 adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:53
L13	344232	multiplex\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:53
L14	4836	(tft adj thin adj film adj transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:51
L15	2751	pixel\$1 adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:54
L16	0	13 adj 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:54

L17	12	13 with 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:56
L18	24	13 same 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:27
L19	54	(multiplexer or switch\$3 or distributer) adj (tft adj thin adj film adj transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:45
L20	57775	(sram or static adj memor\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:31
L21	48	14 with 20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 15:08
L26	0	20 and 19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:44
L27	39	3 and 19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:44
L31	2	(multiplexer or distributer) and (tft adj thin adj film adj transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:47
L32	1	("6765549").PN.	USPAT	OR	OFF	2005/10/05 14:48
L33	0	("03and32").PN.	USPAT	OR	OFF	2005/10/05 14:48
L34	1	3 and 32	USPAT	OR	OFF	2005/10/05 14:48

L35	54	switch\$3 adj (tft adj thin adj film adj transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 14:52
L36	0	20 with 35	USPAT	OR	OFF	2005/10/05 14:52
L37	0	20 and 35	USPAT	OR	OFF	2005/10/05 14:52
L38	2	3 and 21	USPAT	OR	OFF	2005/10/05 14:53
L39	0	(tft or thin adj film adj transistor\$1) with (static adj memor\$3 or sram) adj connect\$3 with (scan\$3 adj line\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 15:12
L40	0	(tft or thin adj film adj transistor\$1) with (static adj memor\$3 or sram) adj connect\$3 with (scan\$3 adj line\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:12
L41	0	(tft or thin adj film adj transistor\$1) same (static adj memor\$3 or sram) adj connect\$3 same(scan\$3 adj line\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:13
L42	6	(tft or thin adj film adj transistor\$1) same (static adj memor\$3 or sram) same(scan\$3 adj line\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:18
L43	0	(plurality adj multiplexer) and (tft or thin adj film adj transistor\$1) and (static adj memor\$3 or sram) and (scan\$3 adj line\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:19
L44	104	(tft or thin adj film adj transistor\$1) and (static adj memor\$3 or sram) and (scan\$3 adj line\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:26
L45	97	3 and 44	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:21

L46	9	15 and 45	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:21
L47	298	(tft or thin adj film adj transistor\$1) adj (static adj memor\$3 or sram)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:38
L48	0	15 and 47	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:27
L49	7	3 with 47	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:28
L50	8	3 same 47	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:28
L51	364524	3 and 47	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:28
L52	37	3 and 47	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:28
L53	1	52 and multiplex\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:40
L54	1	("20050007352").PN.	US-PGPUB; USPAT	OR	OFF	2005/10/05 15:37
L55	0	1 and 3	USPAT	OR	OFF	2005/10/05 15:37

L56	36461	(tft or thin adj film adj transistor\$1) same 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:39
L57	4	(tft or thin adj film adj transistor\$1) same 3 same 20 same 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:40
L58	61	(tft or thin adj film adj transistor\$1) and 3 and 20 and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 16:12
L59	8	58 and multiplex\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:40
L60	1350	(tft or thin adj film adj transistor\$1) with 20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 16:18
L61	2628	345/87-92:ccls.	USPAT	OR	OFF	2005/10/05 16:14
L62	0	60 same 61	USPAT	OR	OFF	2005/10/05 16:14
L63	2	60 and 61	USPAT	OR	OFF	2005/10/05 16:14
L64	286	(tft or thin adj film adj transistor\$1) adj 20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 16:35
L65	377404	(lcd or liquid adj crystal adj display)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 16:19
L66	43	64 and 65	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 16:19

L67	19	("4773738" "5091722" "5200846" "5225823" "5349366" "5424752" "5471225" "5479283" "5600169" "5642129" "5712652" "5798746" "5828429" "5990629" "6023308" "6194837" "6259846" "6274887" "6392618").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/05 16:35
L68	17	65 and 67	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/05 16:35
L69	17	((tft or thin adj film adj transistor\$1) or memor\$3 or multiplex\$3 or switch\$3) and 68	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 16:36
L70	17	((tft or thin adj film adj transistor\$1) or memor\$3 or multiplex\$3 or switch\$3 or static or sram) and 68	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 16:36